

1. Public Executive Summary

This report describes what has been achieved until now with respect to the design and (partly) use of the new camera architecture in the 2020 3D Media project. Discussed is the status on the camera rig design that has been done, the design of a second generation 10G Ethernet transmission board for integration in a prototype camera, and first preparations to start research on Time of Flight. For resolution enhancement the algorithm proposed in deliverable D4.6 is now being implemented in FPGA.